

IC Technology – The Roadmap Going Forward

Lecturer H.-S. Philip Wong



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Dr. H.-S. Philip Wong currently serves as Vice President of Corporate Research responsible for exploring new technologies for TSMC. Prior to joining TSMC, Dr. Wong is Professor of Electrical Engineering and holds the Willard R. and Inez Kerr Bell Professorship in the School of Engineering at Stanford University. He has 16 years of research and path-finding experience at IBM Research, where many of his early research works have led to product technologies. His research aims at translating discoveries in science into practical technologies and has contributed to advancements in nanoscale science and technology, semiconductor technology, solid-state devices, and electronic imaging. He is the founding Faculty Co-Director of the Stanford SystemX Alliance and Faculty Director of the Stanford Non-Volatile Memory Technology Research Initiative (NMTRI). He has held leadership positions at major multi-university research centers of the National Science Foundation and the Semiconductor Research Corporation.

Venue

MIRC Internation Conference Room

電資大樓國際會議廳

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Materials research will play an increasingly important role for developing future IC technology. In this talk, I will give an overview of IC technology and the roadmap going forward.

